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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/919,359	07/31/2001	Vernon E. Rowe	CYTW 003	2539

7590 05/31/2005
Garlick & Harrison
P.O. Box 160727
Austin, TX 78716-0727

EXAMINER

BRADLEY, MATTHEW A

ART UNIT	PAPER NUMBER
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2187

DATE MAILED: 05/31/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 09/919,359	Applicant(s) ROWE ET AL.	
	Examiner Matthew Bradley	Art Unit 2187	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 31 July 2001.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-5 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-5 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-5 are rejected under 35 U.S.C. 102(b) as being anticipated by Ishimoto et al (U.S. 6,101,586). (The Microsoft Computer Dictionary Fifth Edition is used as evidentiary support.)

As per independent claim 1, Ishimoto et al teaches,

- “memory for storing access logic and parameters”, (Figure 1)
- “circuitry for executing the access logic in relation to the parameters that grant access to memory only to resident applications on the computer that are authorized to gain access to the memory”, (Figure 1 and column 6 lines 16-36).

As per dependent claim 2, Ishimoto et al teaches, “port access is only granted to external sources identified as known good external sources within the memory access parameters”, (column 11 lines 15-23). *The Examiner notes that Ishimoto teaches signals coming from external devices other than the internal CPU shown in Figure 1. This request from the external source would be made through a device that would be connected to the existing CPU via ports. Thus*

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access is granted via the ports to the external source from the CPU when the CPU identifies the source as a known good source.

As per dependent claim 3, Ishimoto et al teaches, "the logic creates upon execution by the processor, a plurality of filters that block access to memory", (column 4 lines 29-65 and column 6 lines 16-36). *The Examiner notes that the ordinary definition of a filter is a piece of hardware that selectively passes certain elements of a signal and eliminates or minimizes others. The comparator of Figure 4, taught by Ishimoto, would compare to items to determine whether access was to be granted. The filters disclosed by Rowe et al, operate by taking an input and comparing it against the tables for allowance or disallowance of access. Thus, Ishimoto et al teaches this process of comparing with the comparators.*

As per dependent claim 4, Ishimoto et al teaches, "a plurality of caches that operate with the plurality of filters to determine, on a prompt basis, whether a routine, whether internal or external, is to be given access to memory", (column 4 lines 29-65 and column 6 lines 16-36). *The Examiner notes that as discussed supra, the comparators taught by Ishimoto et al work in conjunction with the area tables, items 15 and 16a, to determine access rights. These tables would perform the same function of the caches disclosed in Rowe et al.*

As per independent claim 5, Ishimoto et al teaches,

- "a method for protecting a computer system from attacks by hackers, examining access logic in relation to an application

seeking access to a specified system element", (column 6 lines 16-36).

- o "determining whether to allow access by the application", (column 4 lines 29-65 and column 6 lines 16-36).

The Examiner notes that the operations taught by Ishimoto et al. would be executed in a systematic way thus creating a method or series of instructions to follow.

Conclusion

The prior art made of record and not relied upon are as follows:

1. U.S. Patent No. 5,890,189 Nozue et al. teach a memory management and protection system.
2. U.S. Patent No. 5,396,609 Schmidt et al. teach a method of protecting a computer system from unauthorized entry.
3. U.S. Patent No. 3,264,615 Jones Henry G et al teach a memory protection system consisting of the monitoring of addressable memory.
4. U.S. Patent No. 6,754,691 Gomi et al teach a system for protecting an environment from unauthorized execution from agents not known to the environment.
5. Microsoft Computer Dictionary, 5th Edition, 2002, Microsoft Press, pp. 213-215 teach of the definition for firewall and filter.

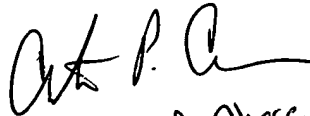
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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Matthew Bradley whose telephone number is (571) 272-8575. The examiner can normally be reached on 7:30-4:00 M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Donald A. Sparks can be reached on (571) 272-4201. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).


CPC/mb


Christian P. Chace
Primary Examiner